

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	DEFECT DIAGNOSIS FOR SEMICONDUCTOR INTEGRATED CIRCUITS							
Application Number : Date : First Named Applicant: James W. Adkisson Confirmation Number: Attorney Docket Number: BUR920040092US1								
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Submitted By:	Elec. Sign.	Sign. Capacity						
Richard M. Kotulak Esq. Registered Number: 27712	/Richard M. KOTULAK, Reg. #27712/	Attorney						

Documents being submitted:	Files
us-ids	BUR920040092US1-usidst.xml us-ids.dtd us-ids.xsl
us-assignment	BUR920040092US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920040092US1Assign1of3.tif BUR920040092US1Assign2of3.tif BUR920040092US1Assign3of3.tif
us-request	BUR920040092US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920040092US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	BUR920040092US1Dec1of3.tif
us-declaration	BUR920040092US1Dec2of3.tif
application-body	BUR920040092US1spec-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920040092US1FIG1of3.tif BUR920040092US1FIG2of3.tif BUR920040092US1FIG3of3.tif
us-declaration	BUR920040092US1Dec3of3.tif

Comments